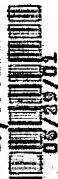


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11011 U.S. PTO
09/896221

Class	Subclass	ISSUE CLASSIFICATION

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PATENT NUMBER

U.S. UTILITY Patent Application

M.S.

O.I.P.E.

PATENT DATE

SCANNED

 603
 O.A. *CH*

APPLICATION NO. 09/896221	CONT/PRIOR	CLASS 709 712	SUBCLASS 32	ART UNIT 2151 2183	EXAMINER <i>Chen, Eddie</i> <i>TSAI</i>
APPLICANTS Jason Gosior Colin Broughton Phillip Jacobsen John Sobota					
TITLE System on chip architecture					

PTO-2040
12/93

ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
712	32	712	32				
INTERNATIONAL CLASSIFICATION							
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	(Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
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